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Docket Number

40404.30/tt

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Hiroshi OJI  Serial No.: Currently unknown  Filing or 371(c) Date: Concurrently herewith  Title: SEMICONDUCTOR DEVICE HAVING HIGH-K GATE DIELECTRIC LAYER AND METHOD FOR MANUFACTURING THE SAME	International Application No.: PCT/JP2005/11260   International Filing Date: June 20, 2005
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**GENERAL AUTHORIZATION/REQUEST TO CHARGE PAYMENT OF FEES**

Mail Stop PCT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

With reference to the subject application, and pursuant to 37 C.F.R. § 1.136, Applicant hereby authorizes and requests the Commissioner to treat any correspondence requiring a petition for extension of time as containing such a request therefor for the appropriate length of time. This general authorization is effective during the pendency of this application, including any division or continuing application therefrom.

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The Commissioner is authorized to charge payment of any fees or credit any overpayment to Deposit Account No. 50-1353.

Respectfully submitted, .

Date: March 21, 2006

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